

Notic of Allowability

Application No.

10/724,070

Examiner

MANSOUR M. SAID

Applicant(s)

HONDA, TAKASHI

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 12/01/03.
2. ☒ The allowed claim(s) is/are 1-4.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
- ☒ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

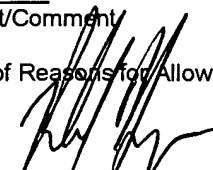
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 12/1/03 & 3/28/05
- ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
- ☐ Examiner's Amendment/Comment
- ☒ Examiner's Statement of Reasons for Allowance
- ☐ Other _____


RICHARD HERPE
SUPERVISORY PATENT EXAMINER
TECHNICAL FIELD 26C0

Allowable Subject Matter

1. **Claims 1-4 allowed.**

2. The following is an examiner's statement of reasons for allowance: Claims 1-4 are allowed since certain key features of the claimed invention are not taught or fairly suggested by prior art. **In claim 1, “wherein a discharge accelerating unit which accelerates a discharge of a capacitive load of said signal line when said positive polarity input voltage decreases is provided for said positive polarity operating circuit, and a charge accelerating unit which accelerates a charge to a capacitive load of said signal line when said negative polarity input voltage decreases is provided for said negative polarity operating circuit”**. The closest prior art Svensson et al. (6,985,142) teaches pulsed driving of capacitive loads to controllable voltage levels, with particular applicability to LCDs ... energy stored in a portion of the capacitive load is recovered during a recovery phase, and Tsuchi (2003/0160749 A1) teaches a differential circuit and an amplifier circuit for reducing an amplitude difference deviation ... the circuit includes a first pair of p-type transistors and a second pair of n-type transistors; however, singularly or in combination with other prior art, fail to anticipate or render the above underlined limitations obvious.

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Fujima (5,821,808) teaches a voltage circuit comprising a reference voltage generator generating a reference voltage, and a voltage stabilizer comprises comparators and P-channel and N-channel transistors.

Janssen et al. (6,384,817 B1) teaches voltages to individual columns of pixels in a color electro-optic display.

Wilcox et al. (6,580,258 B2) teaches a circuit for controlling a switching voltage regulator having a switch including one or more switching transistors and an output adapted to supply current at a regulated voltage to a load including an output capacitor.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mansour M. Said whose telephone number is 571-272-7679. The examiner can normally be reached on Monday through Thursday from 8:30-6:00 P.M. The examiner can also be reached on alternate Friday from 8:30 a.m. to 5:00 p.m. EST. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerpe whose telephone number is 571-272-7681.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to: 571-273-8300 (for Technology Center 2600 only)

Hand-delivered responses should be brought to the Customer Service Window at the Randolph Building, 401, Dulany Street, Alexandria, VA 22314.


Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be

Art Unit: 2629

obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mansour M. Said

6/7/06



RICHARD HJERPE
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TECHNICAL CENTER 2600